

Fig.1

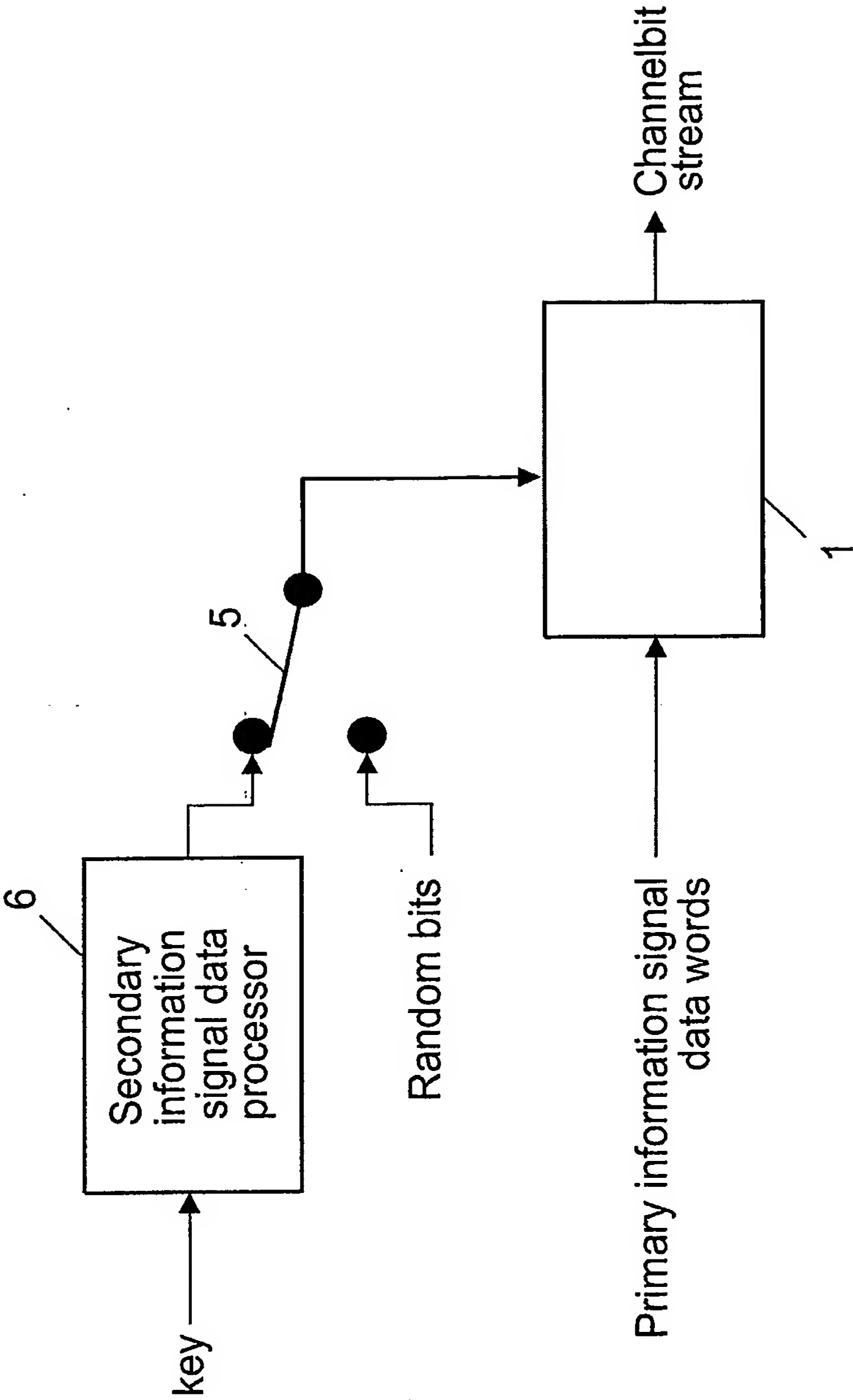


Fig.2

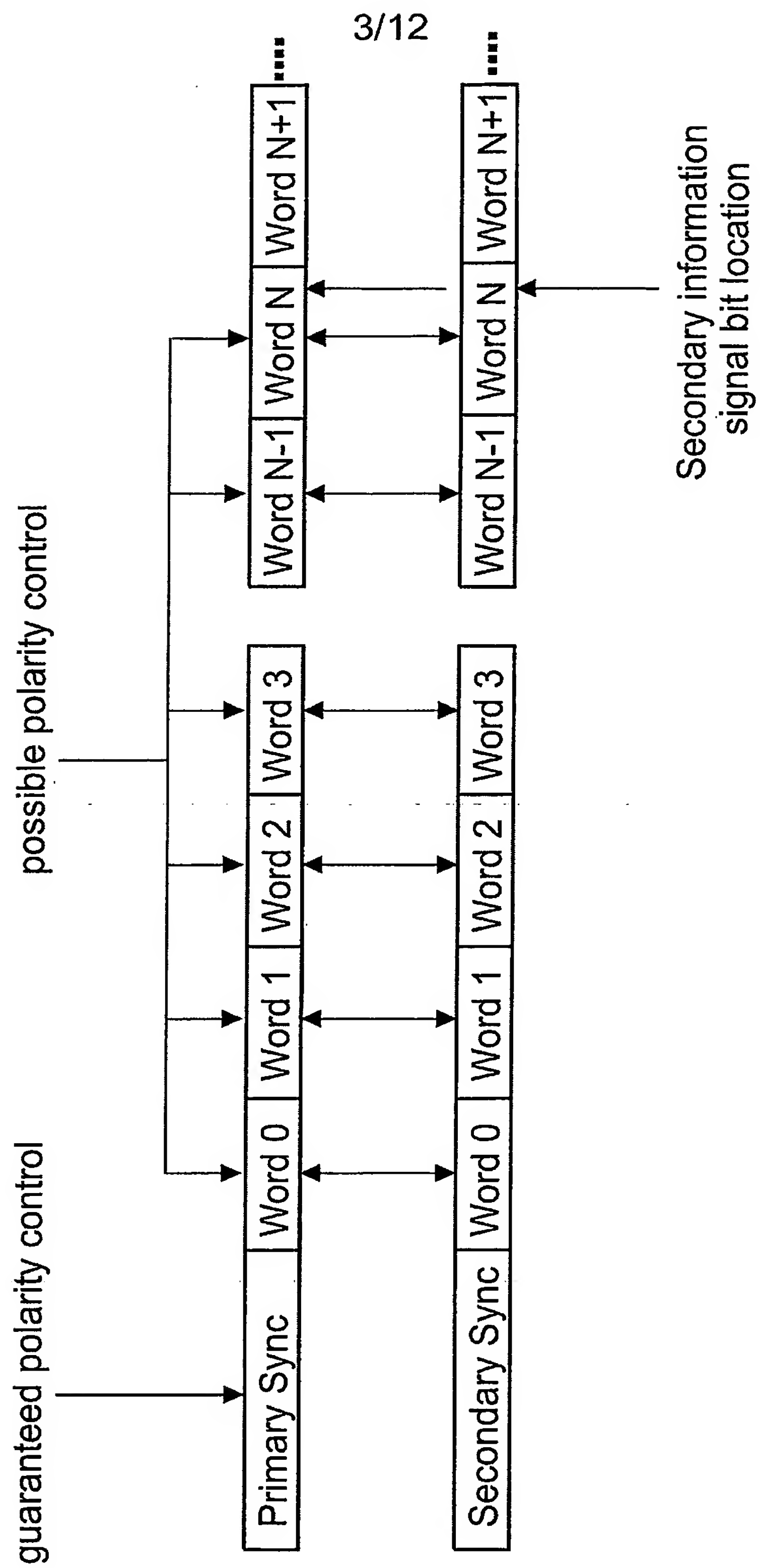


Fig. 3

4/12

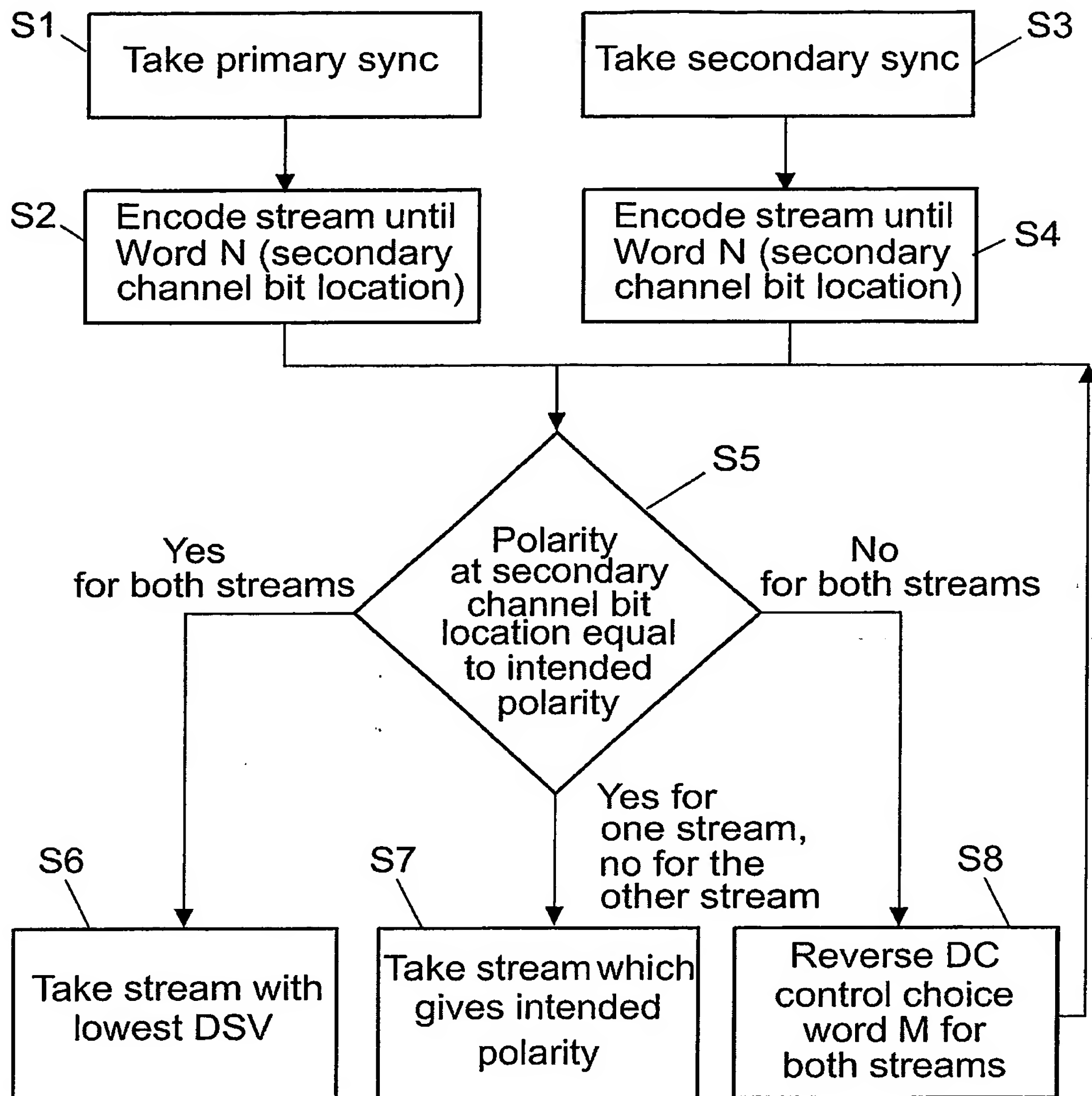


Fig.4

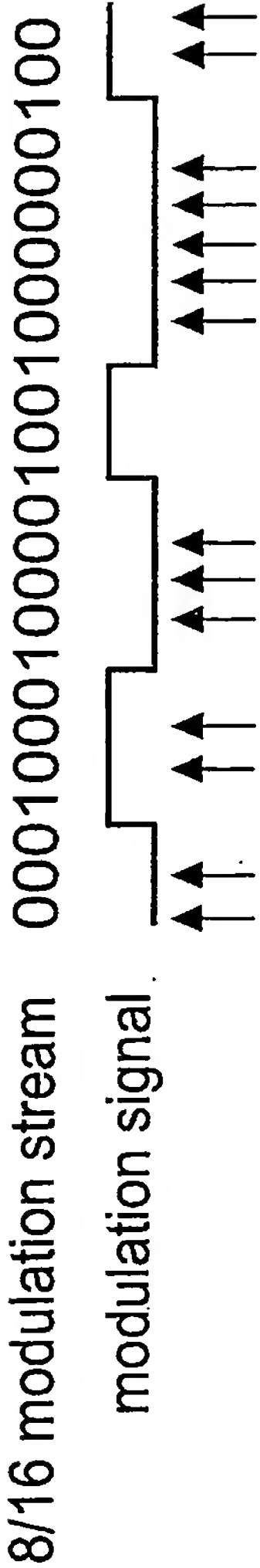


Fig.5

8-bit byte	State 1			State 2			State 3			State 4		
	Code Word		Next	Code Word		Next	Code Word		Next	Code Word		Next
	msb	lsb	State	msb	lsb	State	msb	lsb	State	msb	lsb	State
0	001000000001001		1	0100000100100000		2	0010000000001001		1	0100000100100000		2
1	0010000000010010		1	00100000000010010		1	1000000100100000		3	1000000100100000		3
2	0010000100100000		2	00100000000100000		2	10000000000010010		1	10000000000010010		1
3	0010000001001000		2	0100010010000000		4	0010010001001000		2	0100010010000000		4
4	0010000001001000		2	0010000001001000		2	1000000100100000		2	1000000100100000		2
5	00100000000100100		2	00100000000100100		2	1001001000000000		4	1001001000000000		4
6	00100000000100100		3	00100000000100100		3	1000100100000000		4	1000100100000000		4
...



State 1			
8-bit byte	Code Word		Next
	msb	lsb	State
0	001000000001001	Bit 8	1
1	0010000000010010	Bit 9	1
2	0010000100100000	Bit 2	2
3	0010000001001000	Bit 10	2
4	0010000001001000	Bit 2	2
5	00100000000100100	Bit 9	2
6	00100000000100100	Bit 10	3
...

Fig.6

Sync frame	Code word
0	2
1	88
2	6
...	...

Fig.7a

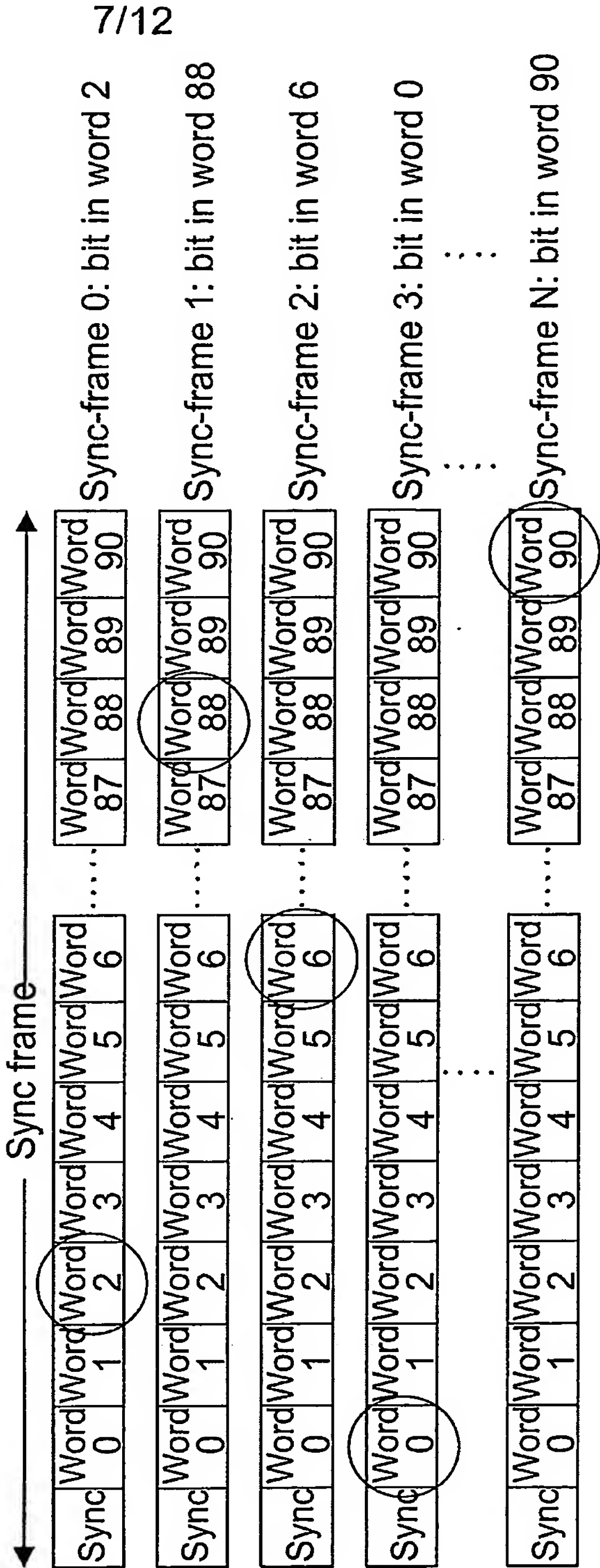


Fig.7b

Rec frame	Code word
0	2
1	3
2	0
...	...

Fig.8a

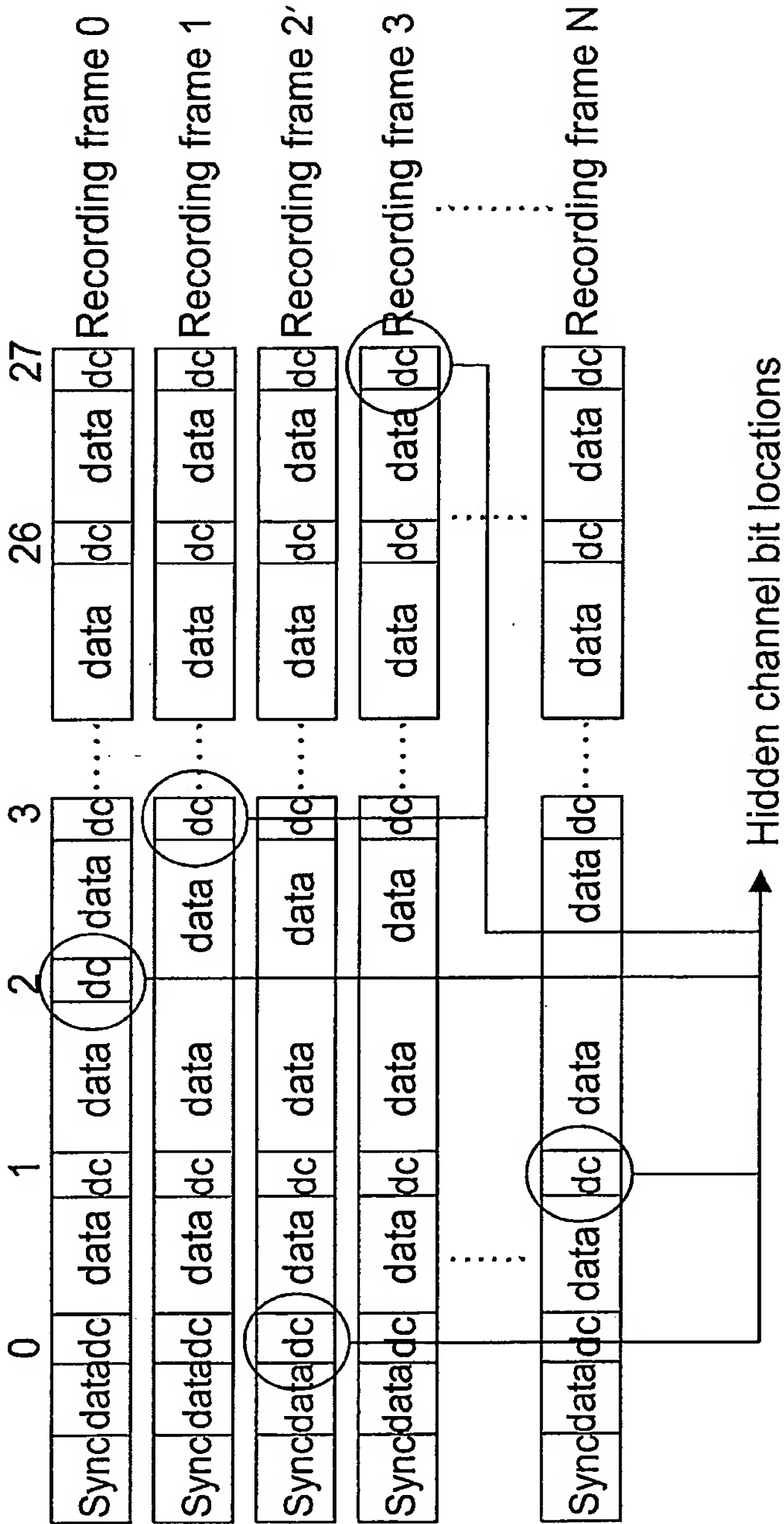


Fig.8b

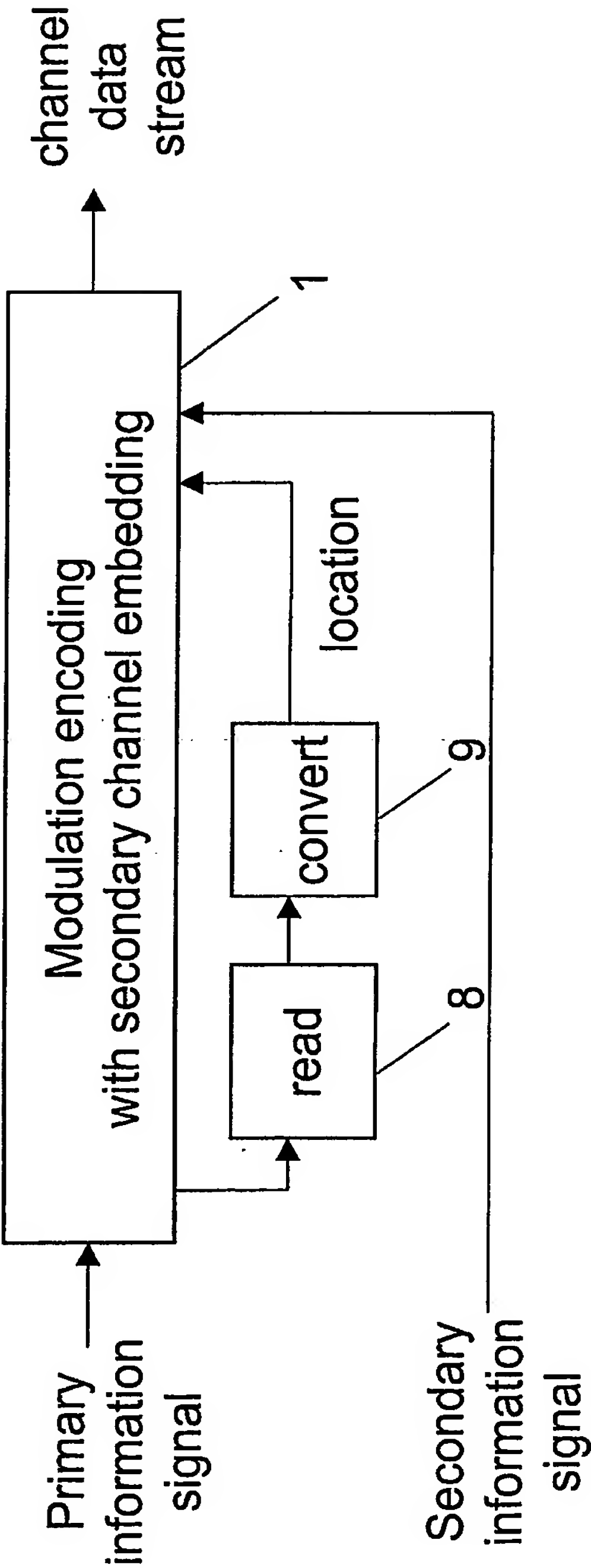


Fig.11

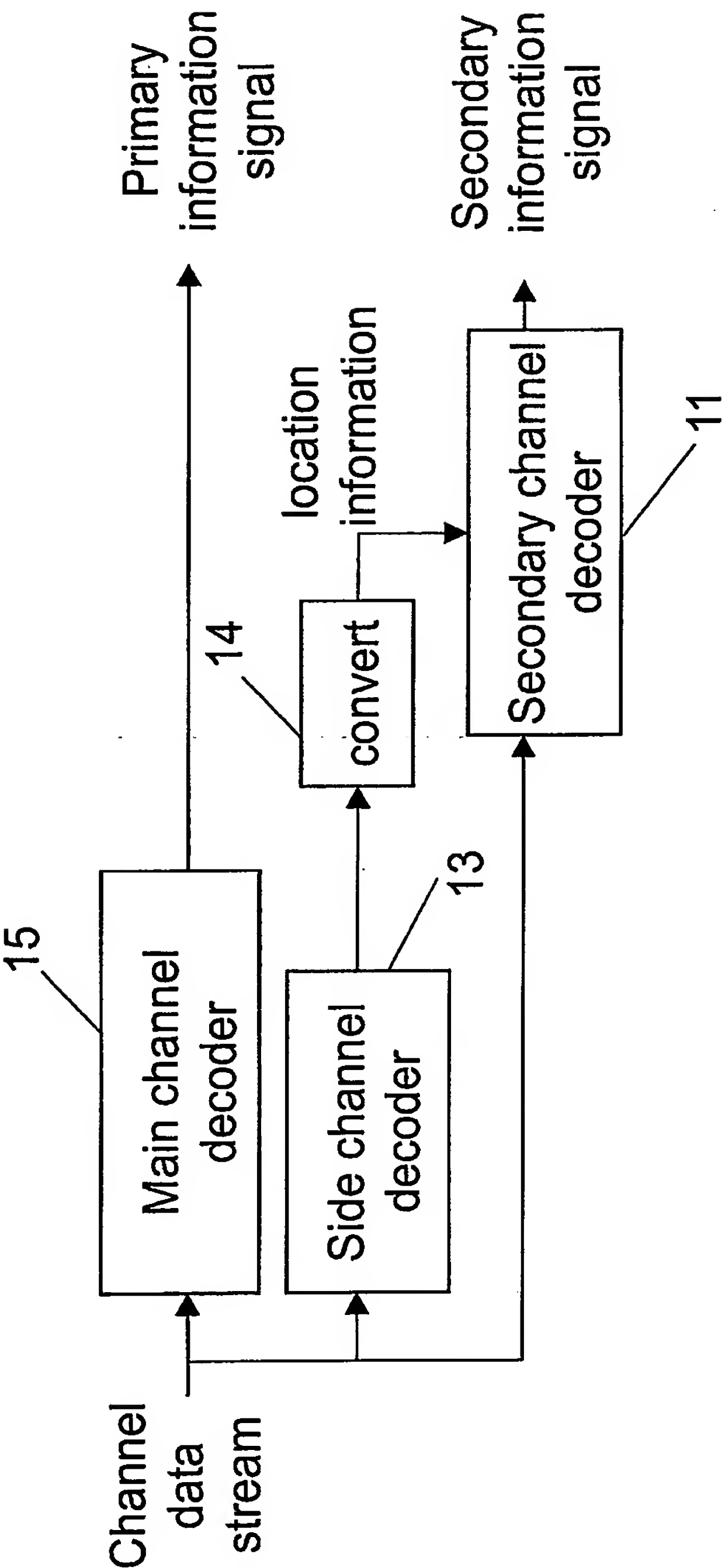


Fig.12

12/12

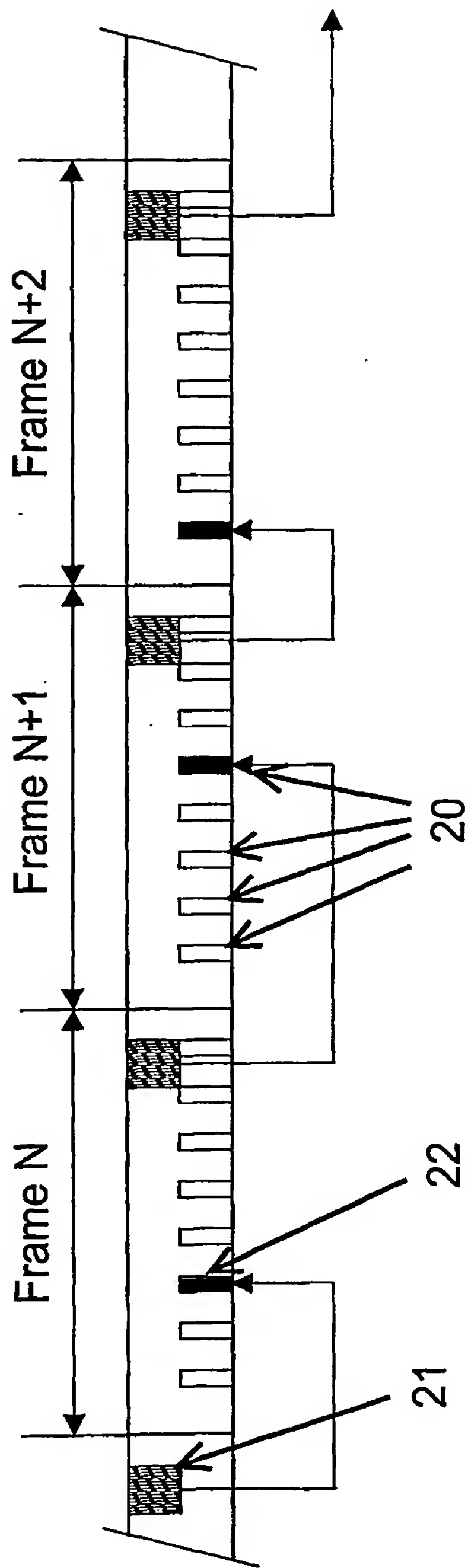


Fig.13